



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Reif et al. Art Unit : 2811
Serial No. : 10/749,096 Examiner : Owens, Douglas W.
Filed : December 30, 2003
Title : MULT-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE HAVING
AN ELECTRICAL SHIELDING PORTION

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF AUGUST 1, 2005

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING OR TRANSMISSION (37 C.F.R. §1.8(a))

I hereby certify that this correspondence is being transmitted via facsimile to Commissioner of Patents or deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to: MS RCE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on the date set forth below.

3- November - 2005
Date of Deposit or Transmission
Cara A. Murphy
Signature
Cara A. Murphy
Typed or Printed Name of Person Signing Certificate